

The input pulse waveform is shown in **Figure 4**. A logical "ON" or "HIGH" is 4-5 volts, and "LOW" is between 0-0.5 volts. The input pulse should have TTL timing characteristics; (i.e., the pulse rise and fall times must be less than 1 microsecond). The noise filtering in the UPD driver requires that the input pulses be stable for a minimum of 10 microseconds.

In the "1 PULSE" mode, the motor will rotate one step for each pulse received at the driver's "PULSE" (STEP) terminal. The motor moves when the trailing edge of the pulse is detected. (**See Figure 2.**) The direction of rotation is controlled by the signal at the "CW/CCW" (DIRECTION) terminal of the driver. When the "CW/CCW" (DIRECTION) terminal is "HIGH", the motor will rotate one step clockwise for each pulse received at the "PULSE" (STEP) terminal. The motor will rotate one step counter-clockwise for each pulse received at the "PULSE" terminal when the signal at the "CW/CCW" (DIRECTION)

terminal is "LOW", or the signal is absent. Care must be taken so the step signal is inactive when the direction signal is changed, or missed steps may result.

Figure 4
INPUT PULSE CHARACTERISTICS

